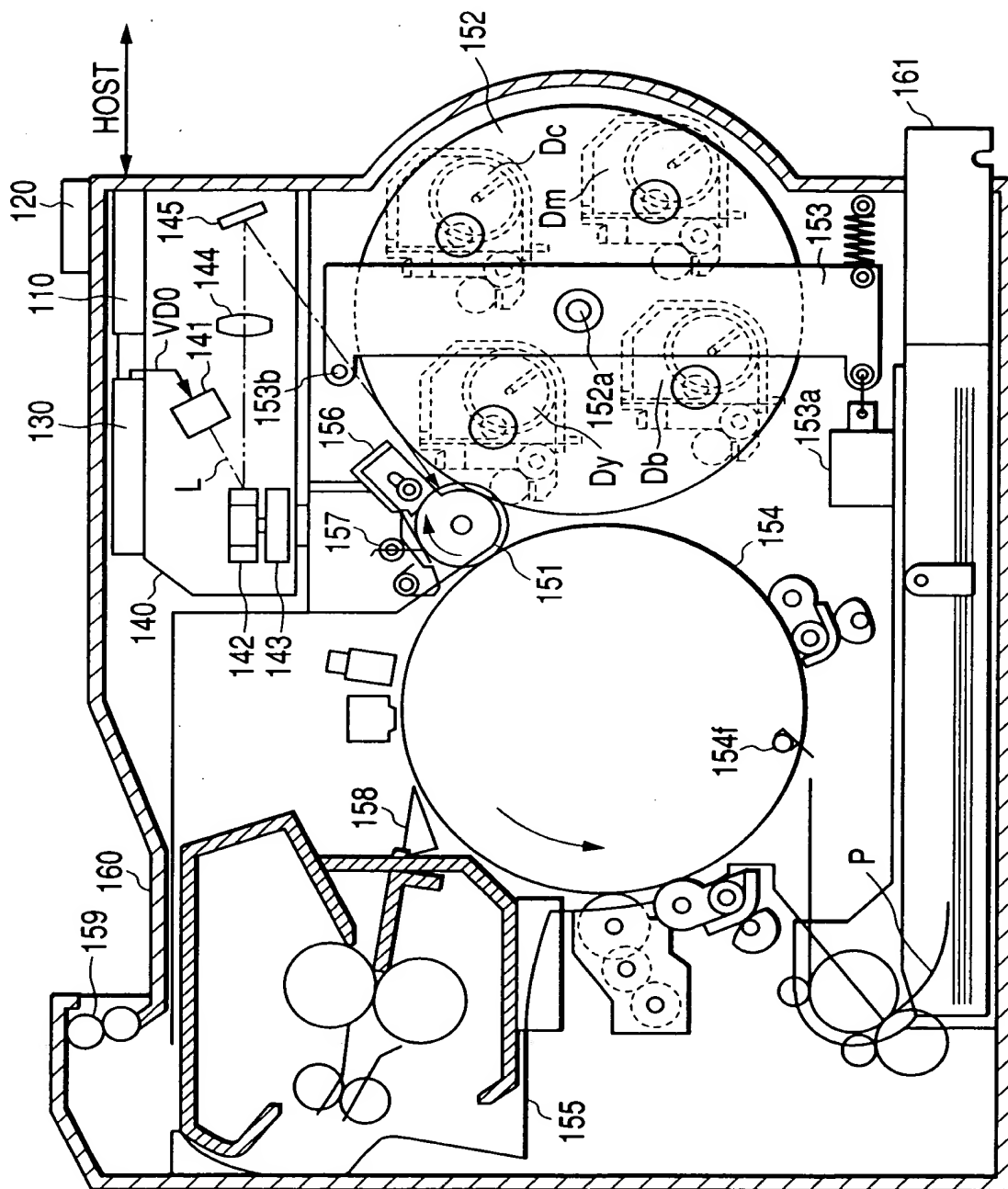


FIG. 1



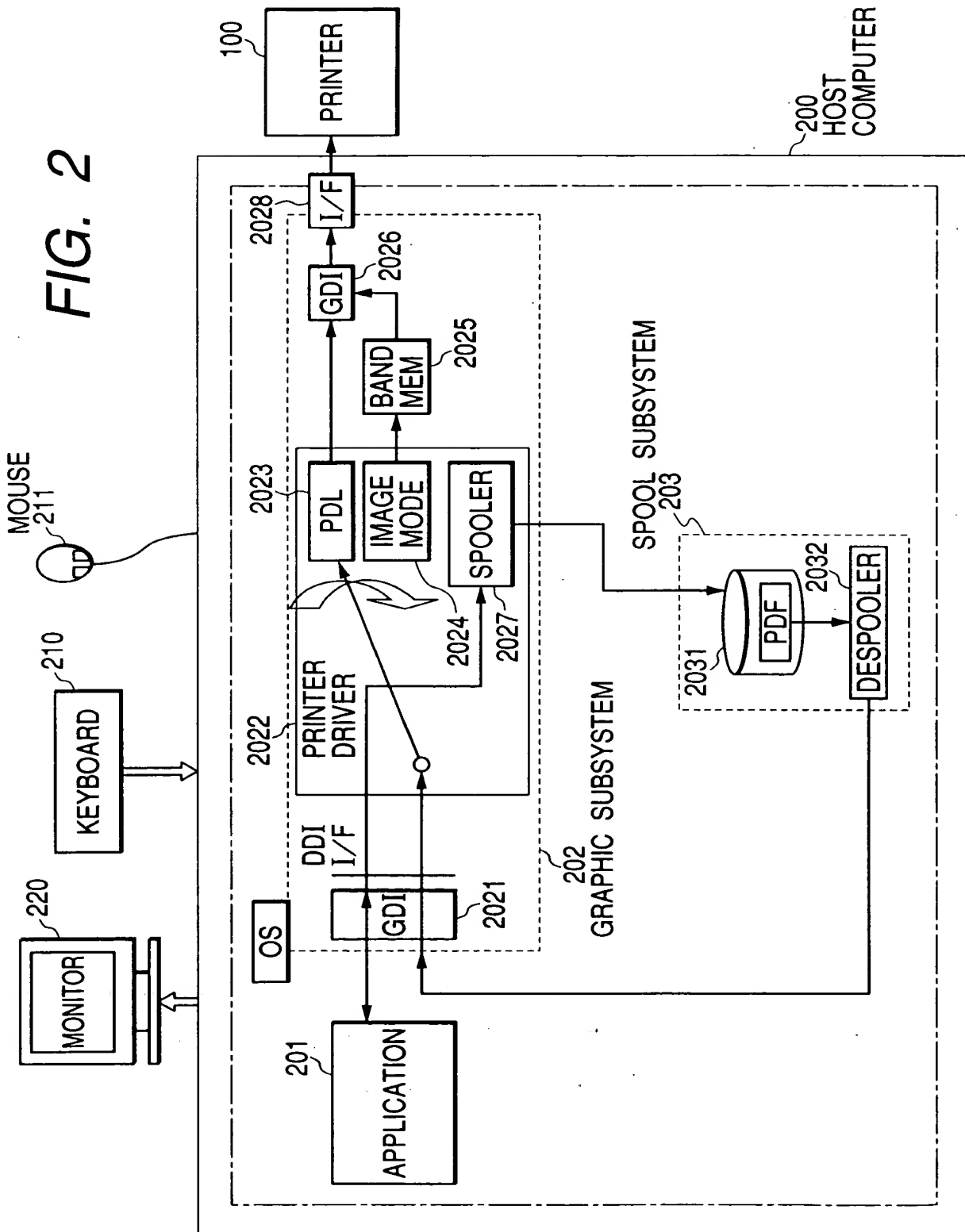


FIG. 3A

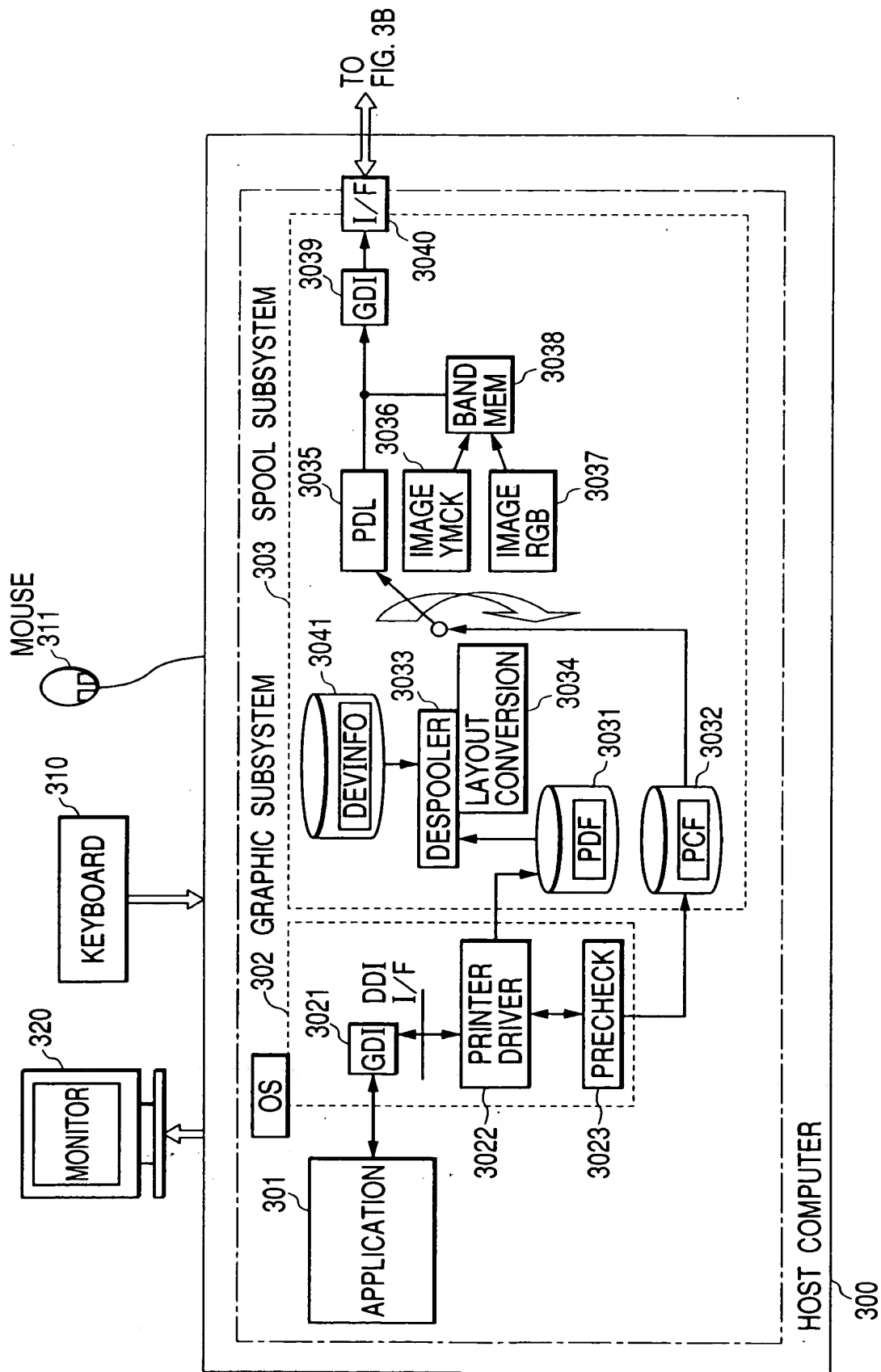


FIG. 4

	DATA SIZE	PROCESSING SPEED	OTHERS
PDL	<ul style="list-style-type: none">• MIN FOR NORMAL PAGE• NO LIMITATIONS	<ul style="list-style-type: none">• HIGH FOR NORMAL PAGE• LOW FOR COMPLICATED GRAPHICS	SHORTCOMINGS IN LOGICAL OPERATION
DEVICE IMAGE	<ul style="list-style-type: none">• PROPORTIONAL TO DRAW AREA• MAX FIXED	<ul style="list-style-type: none">• HIGH• LOW ENGINE SPEED FOR SIMPLE FIGS. & CHRS	SHORTCOMINGS IN LOGICAL OPERATION
RGB24 IMAGE	<ul style="list-style-type: none">• PROPORTIONAL TO DRAW AREA• MAX FIXED	LOW	LOGICAL OPERATION OK

09005003 = 00000000

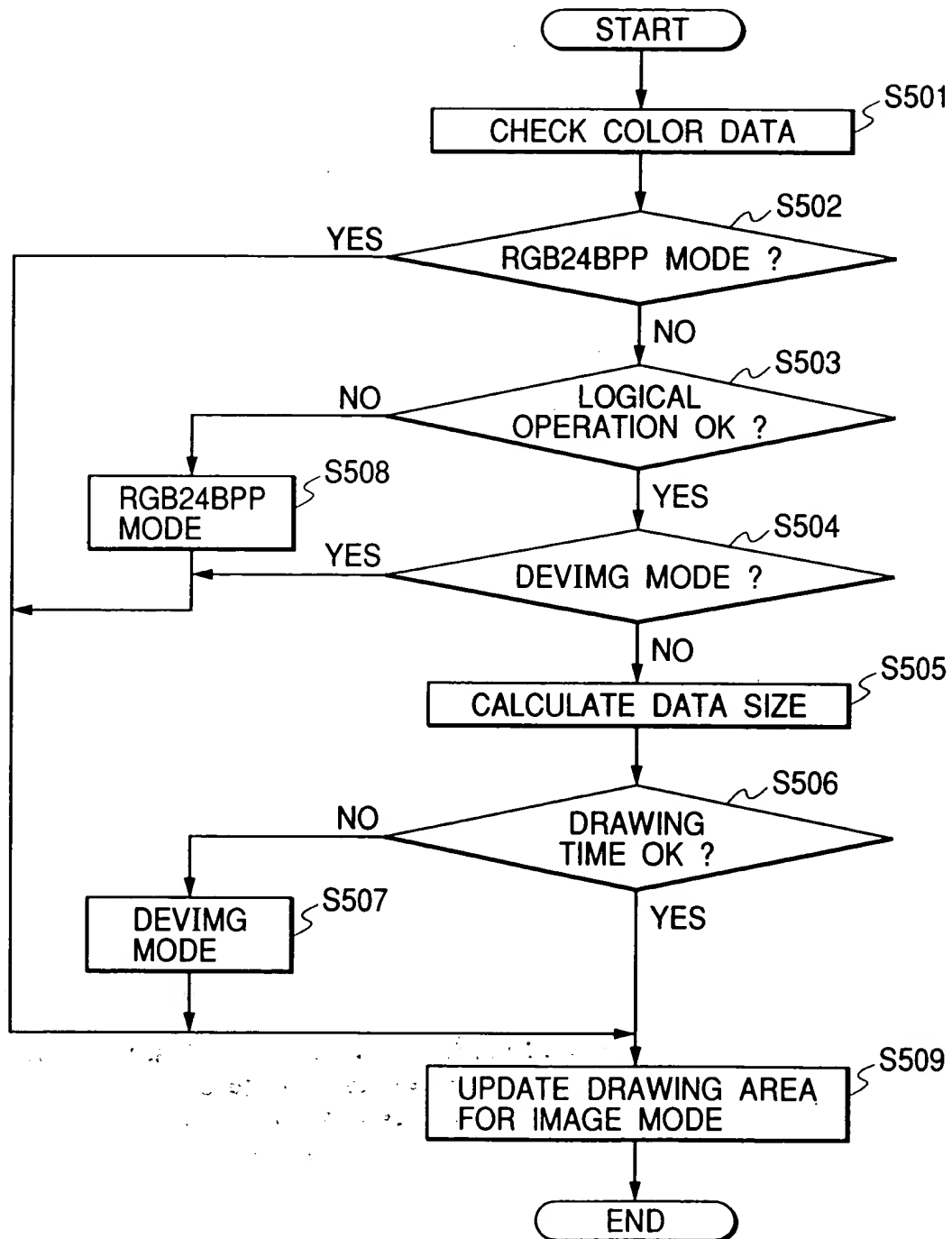
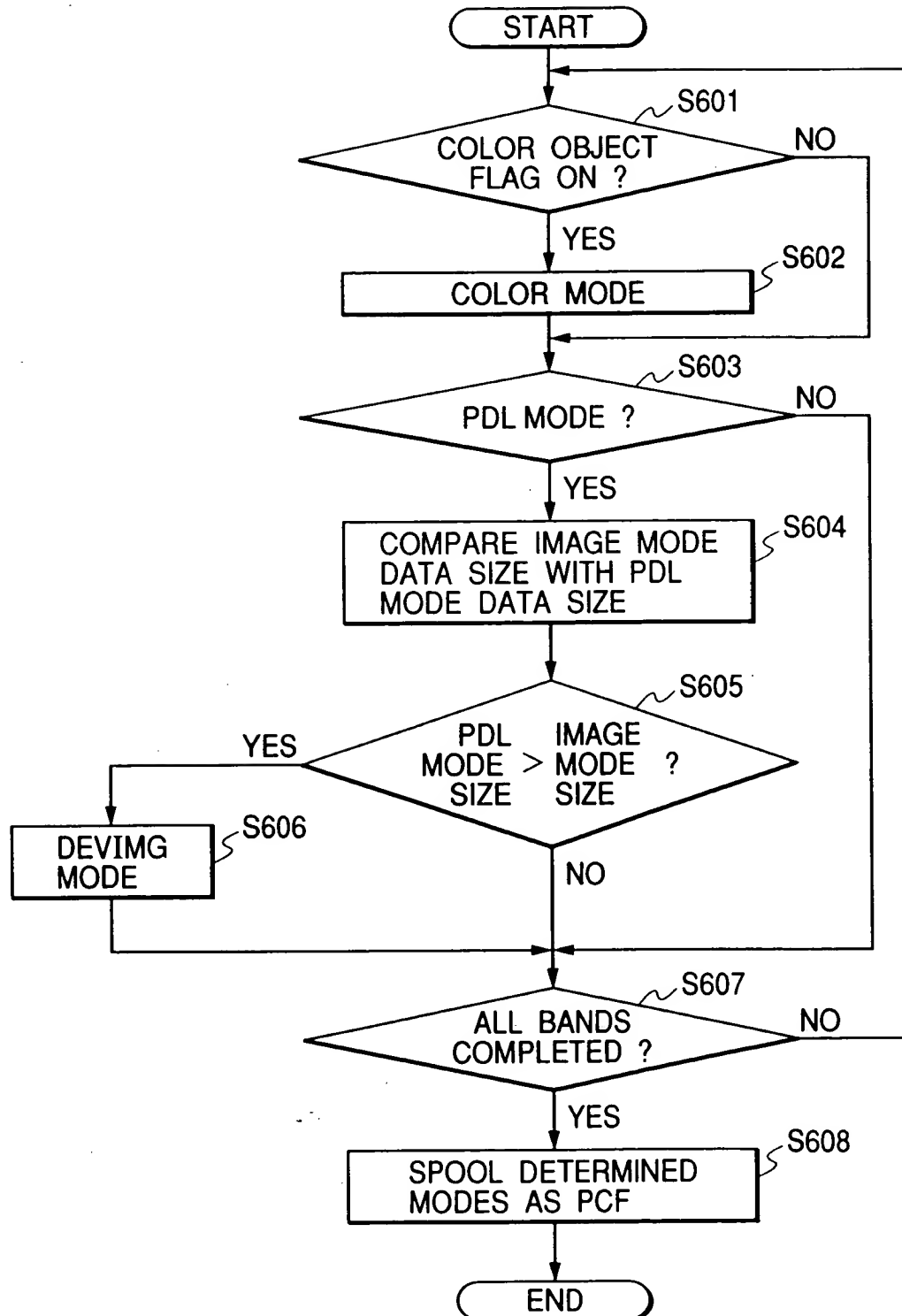


FIG. 6



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FIG. 7

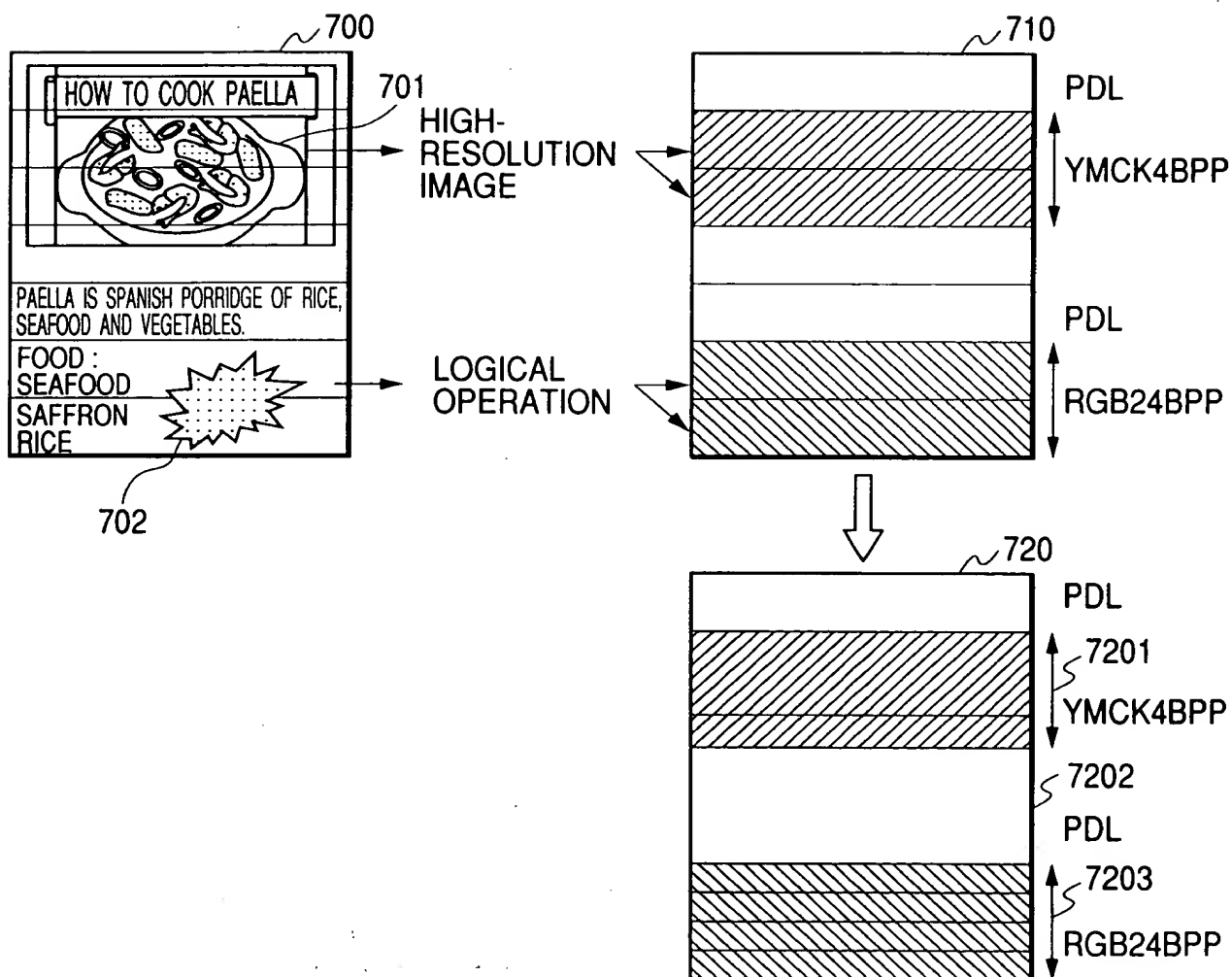


FIG. 8A
FIG. 8B

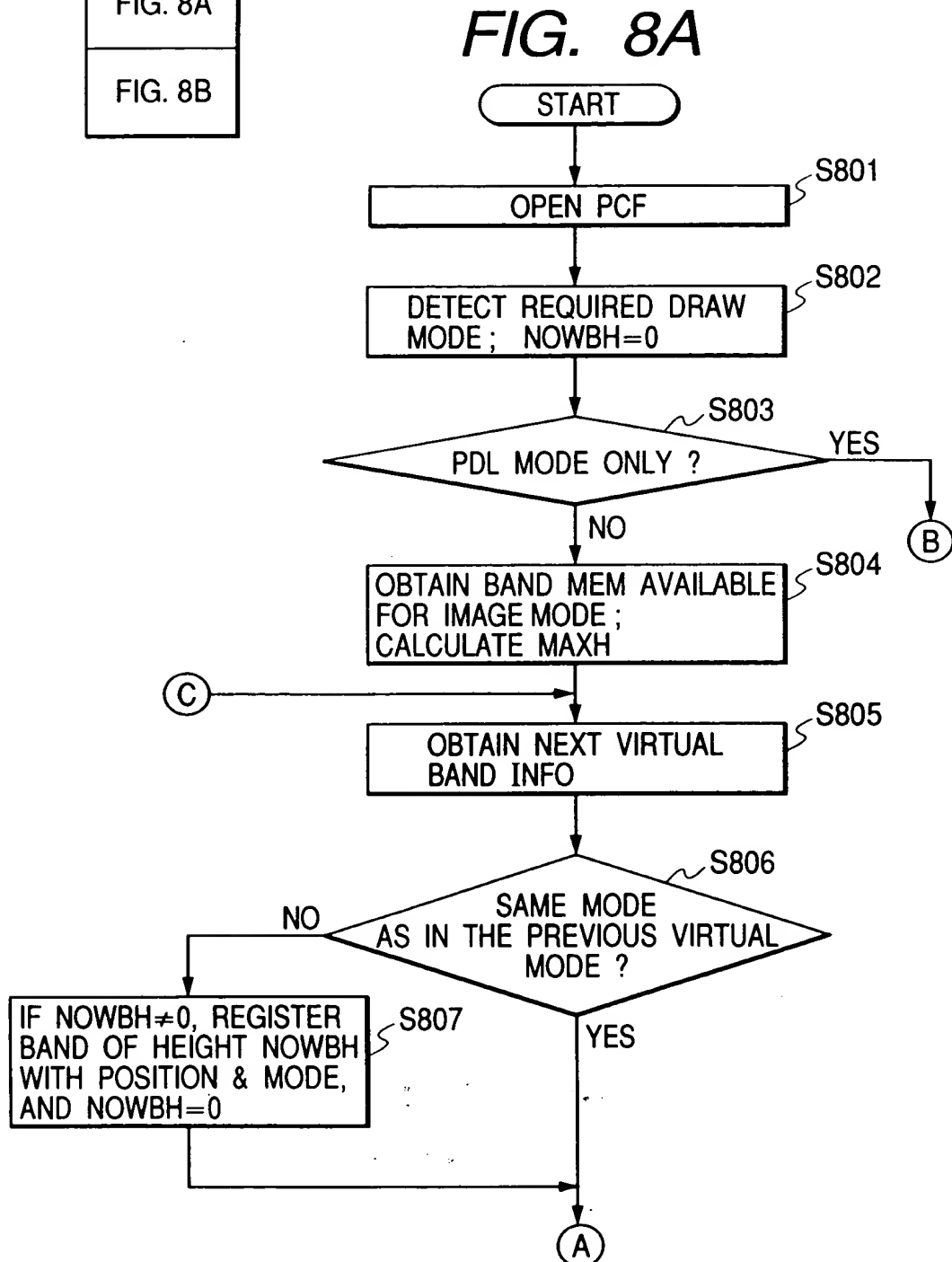
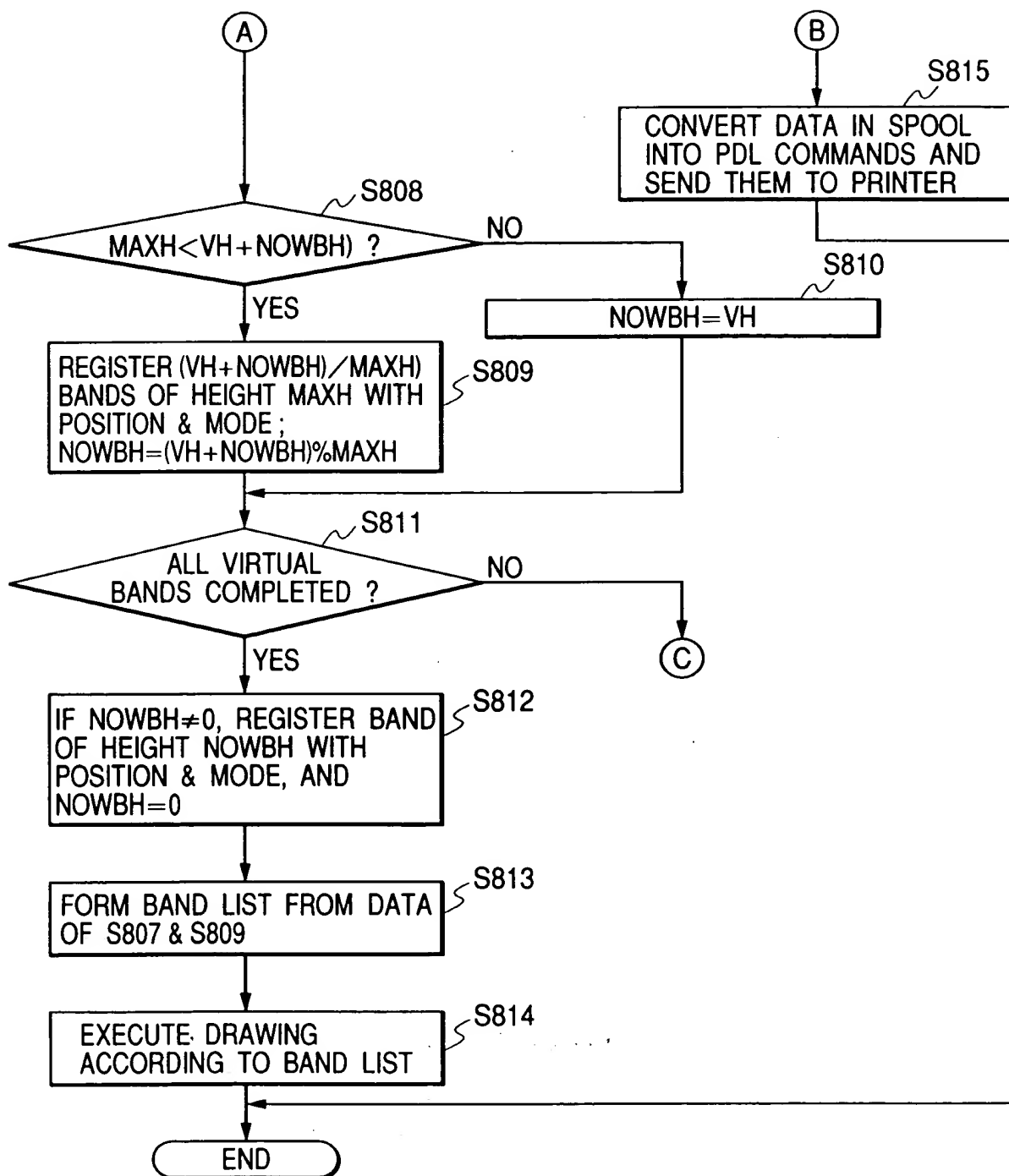
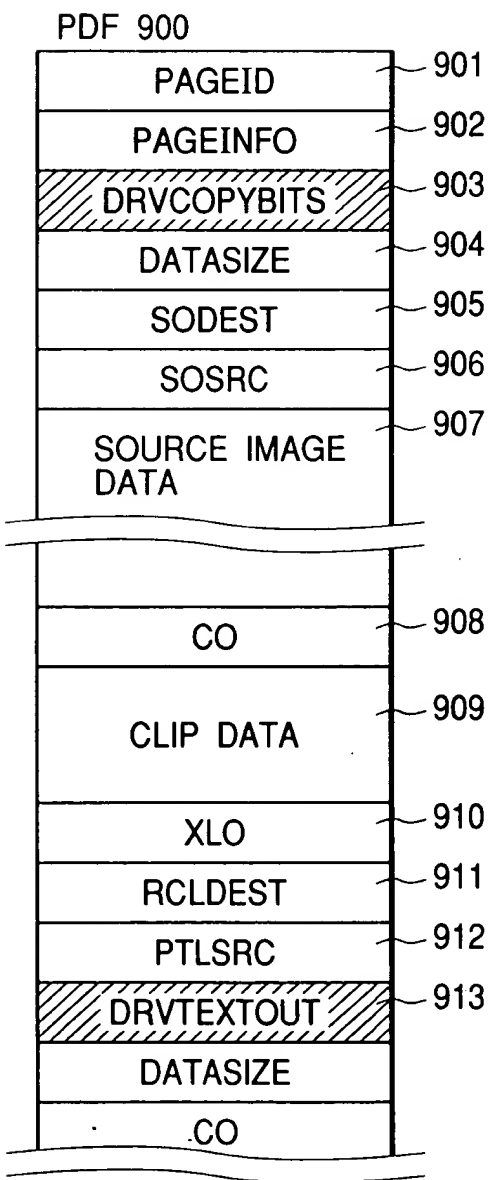


FIG. 8B



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FIG. 9

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FIG. 10

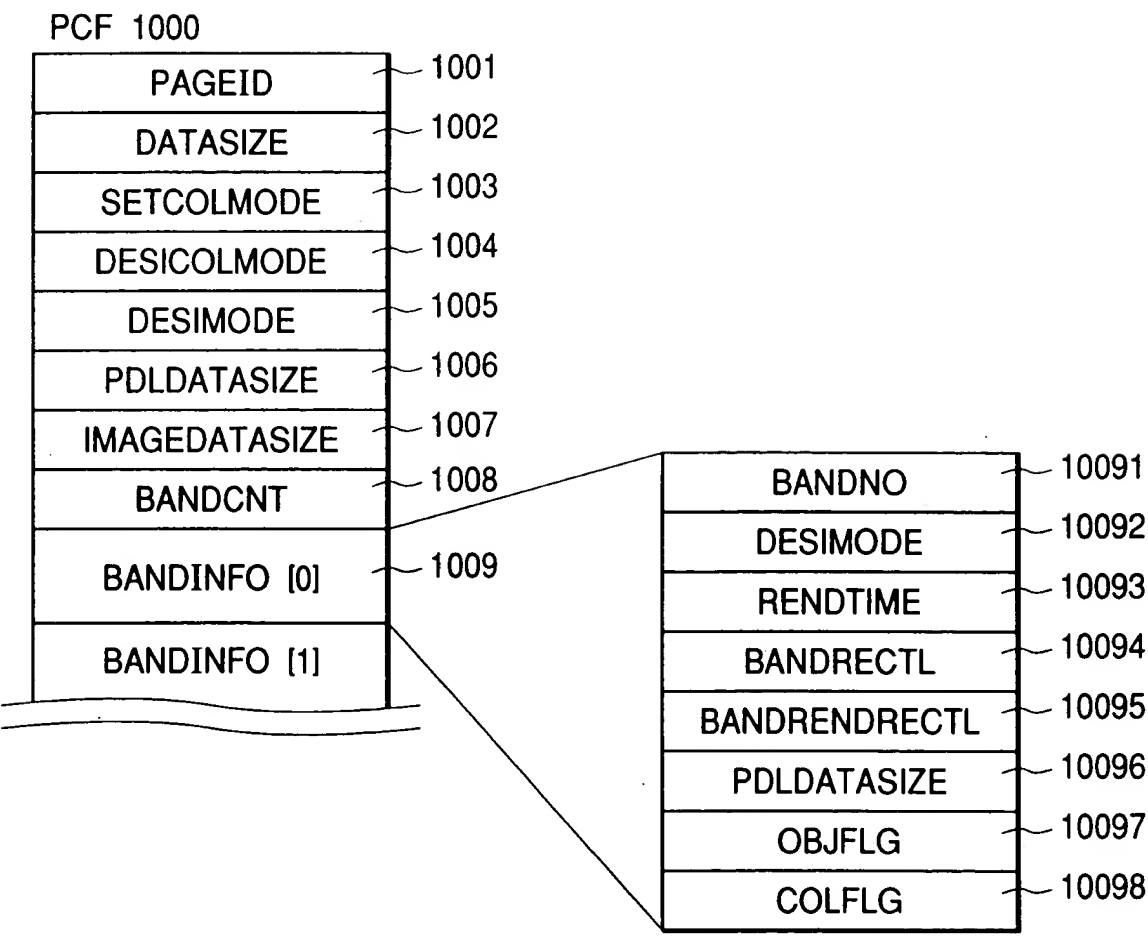


FIG. 11

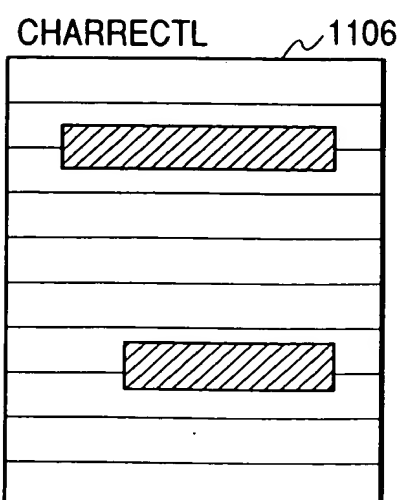
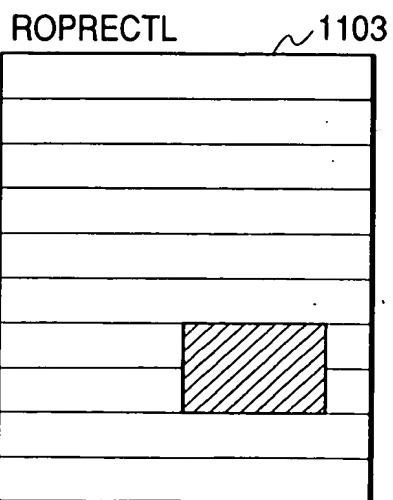
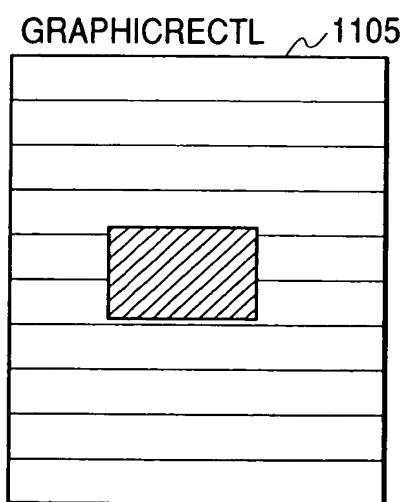
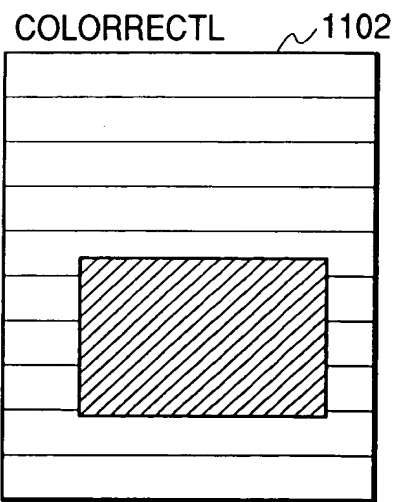
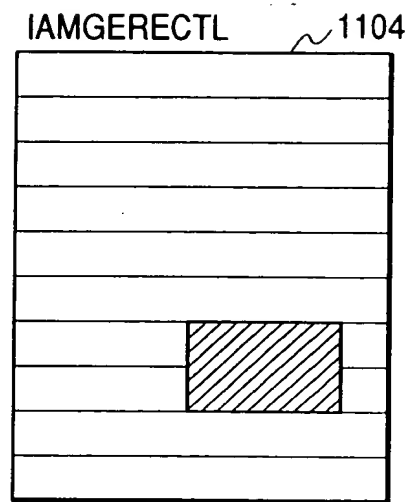
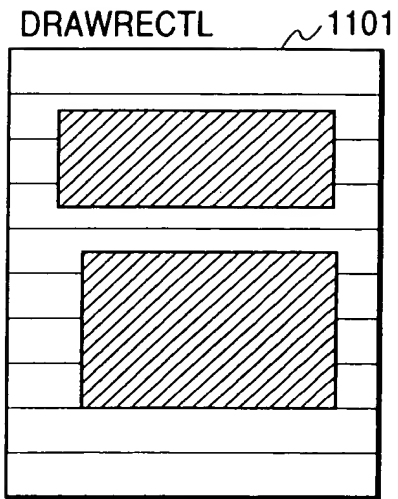
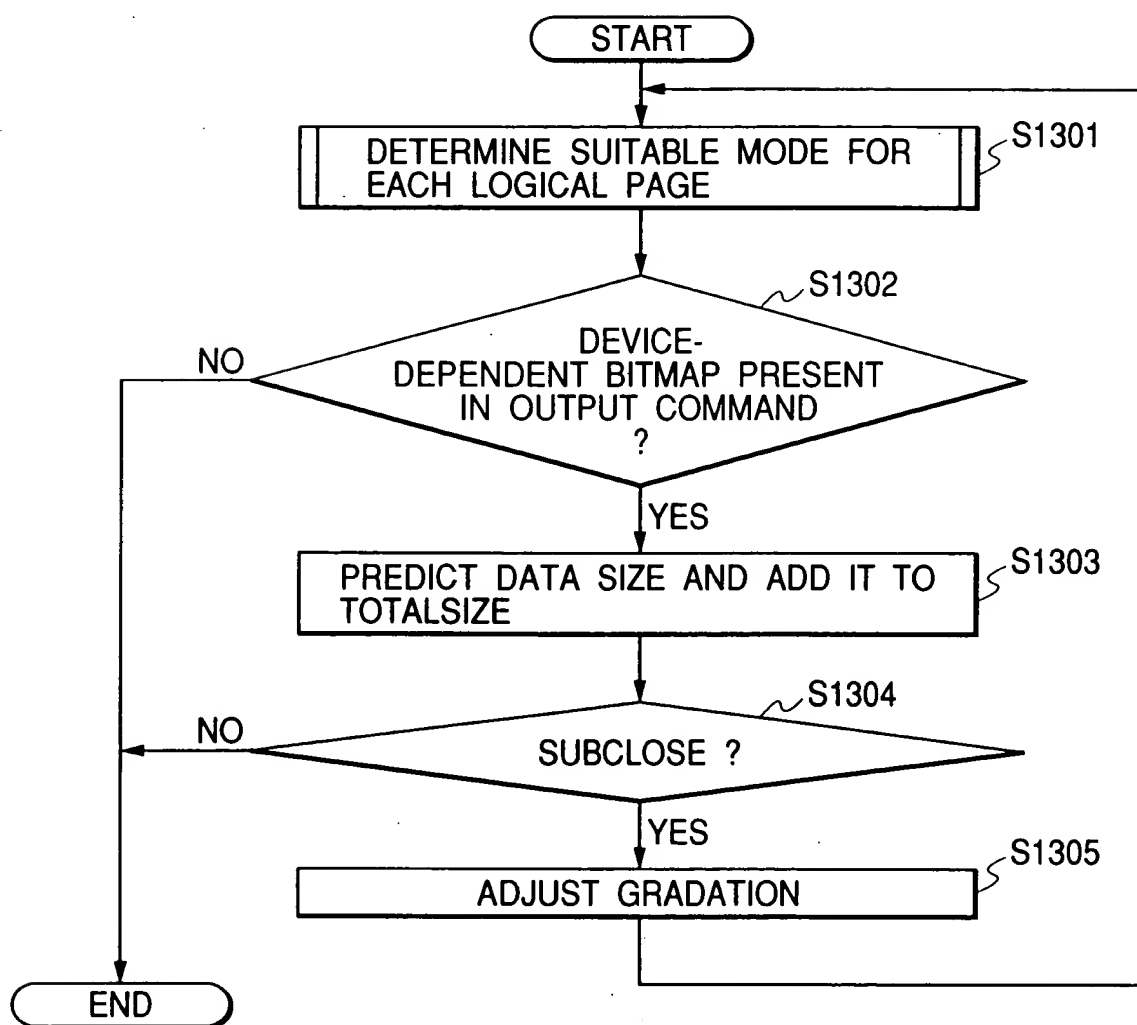


FIG. 13



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FIG. 14

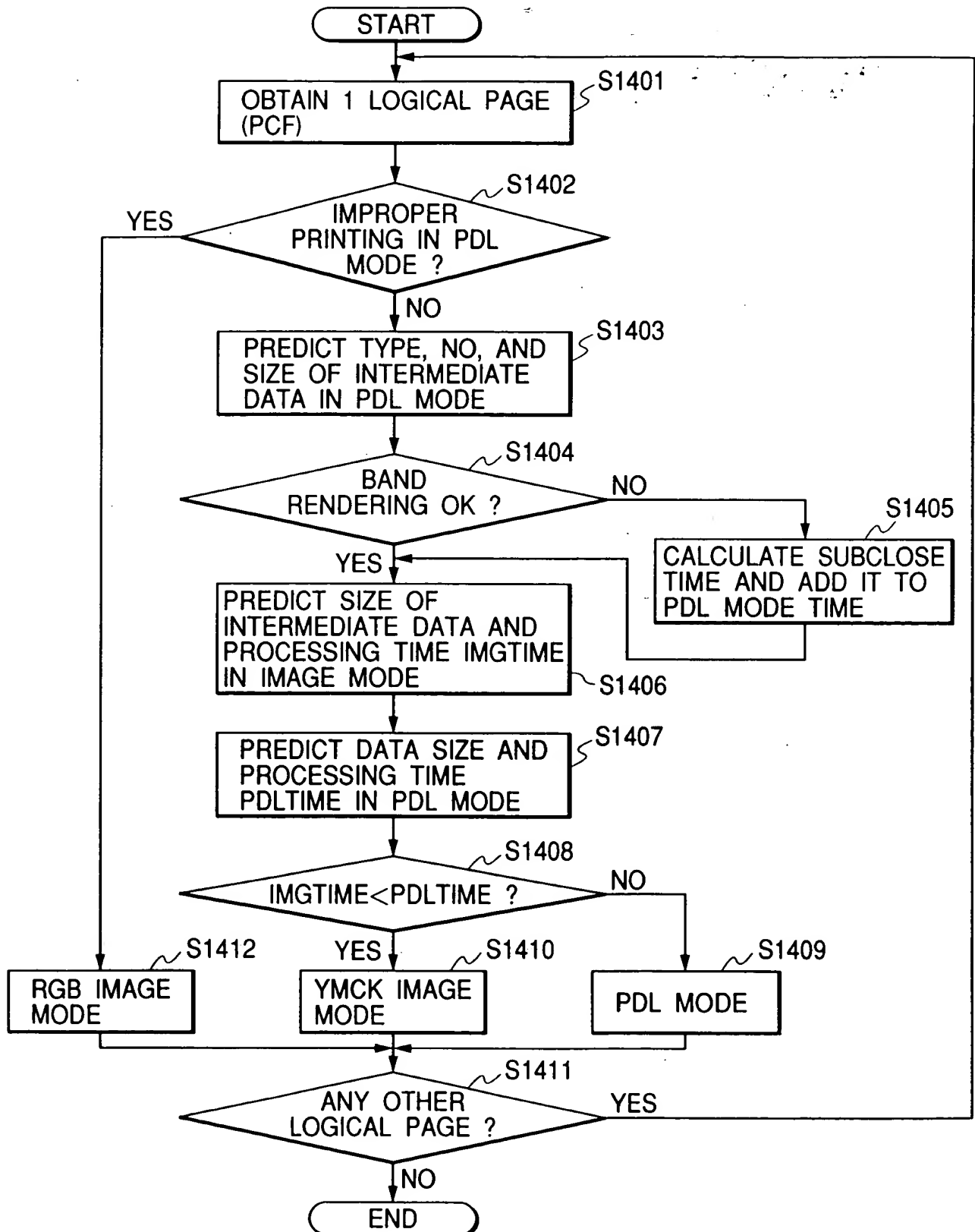


FIG. 15

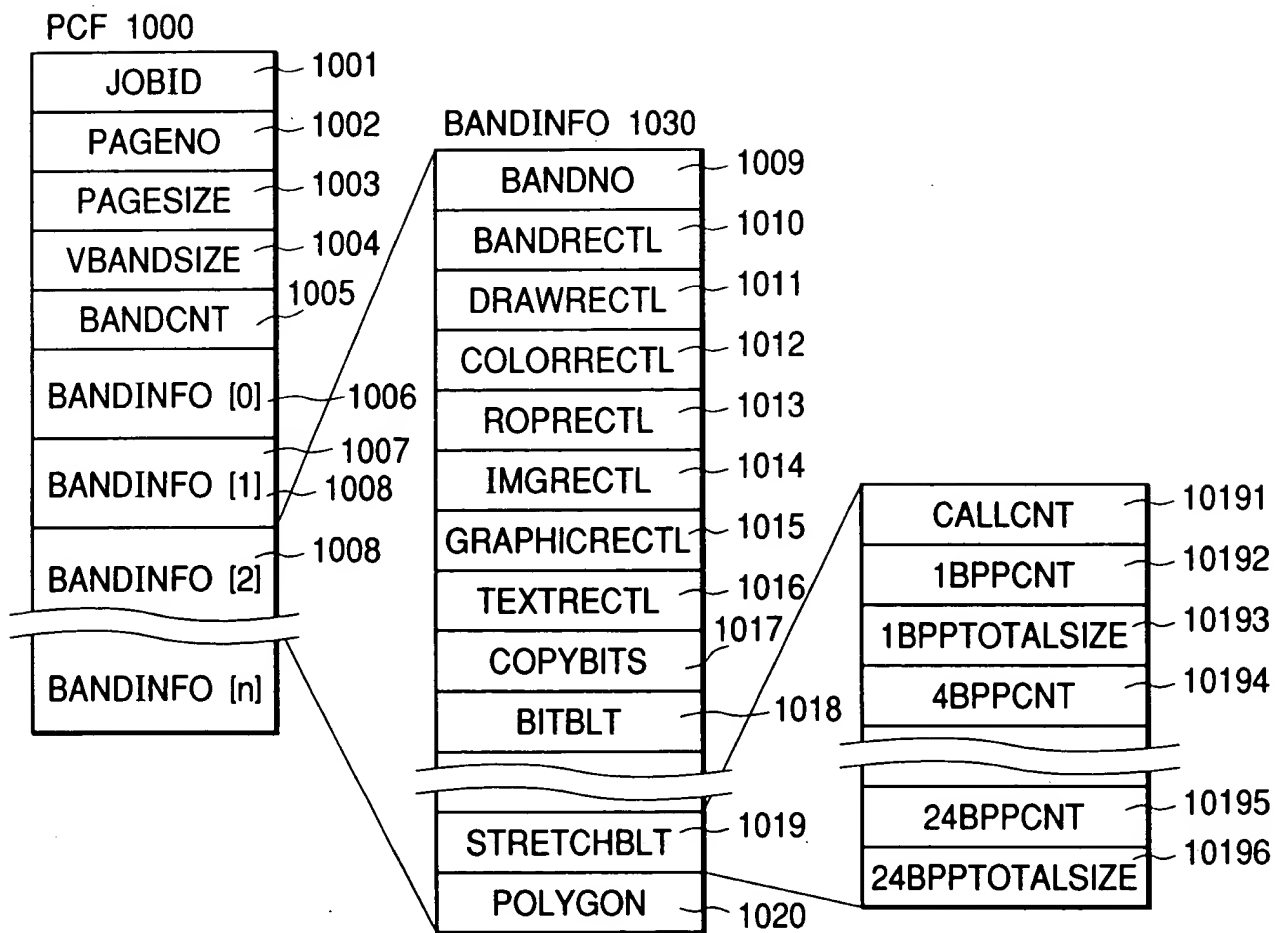


FIG. 16

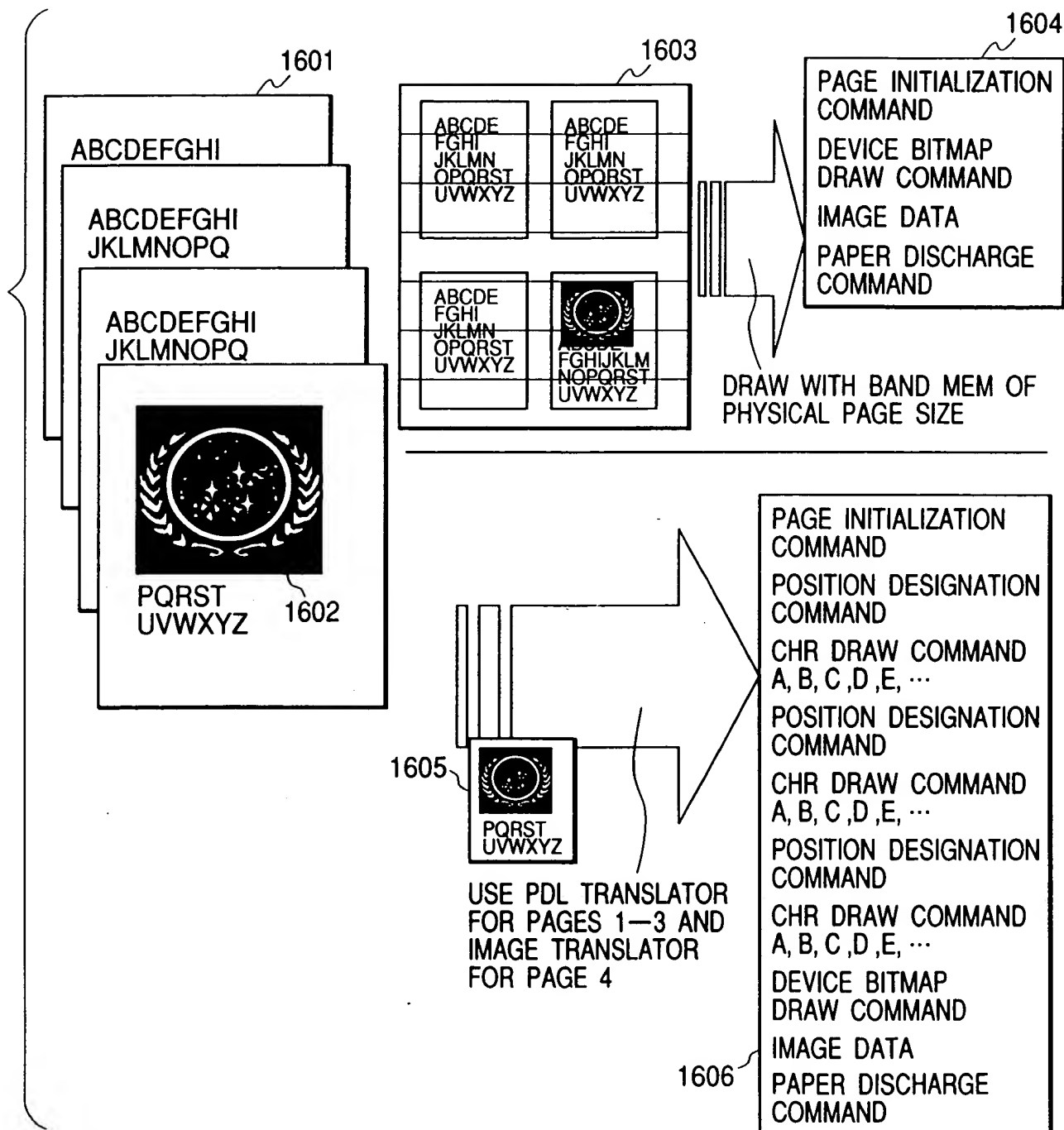


FIG. 17

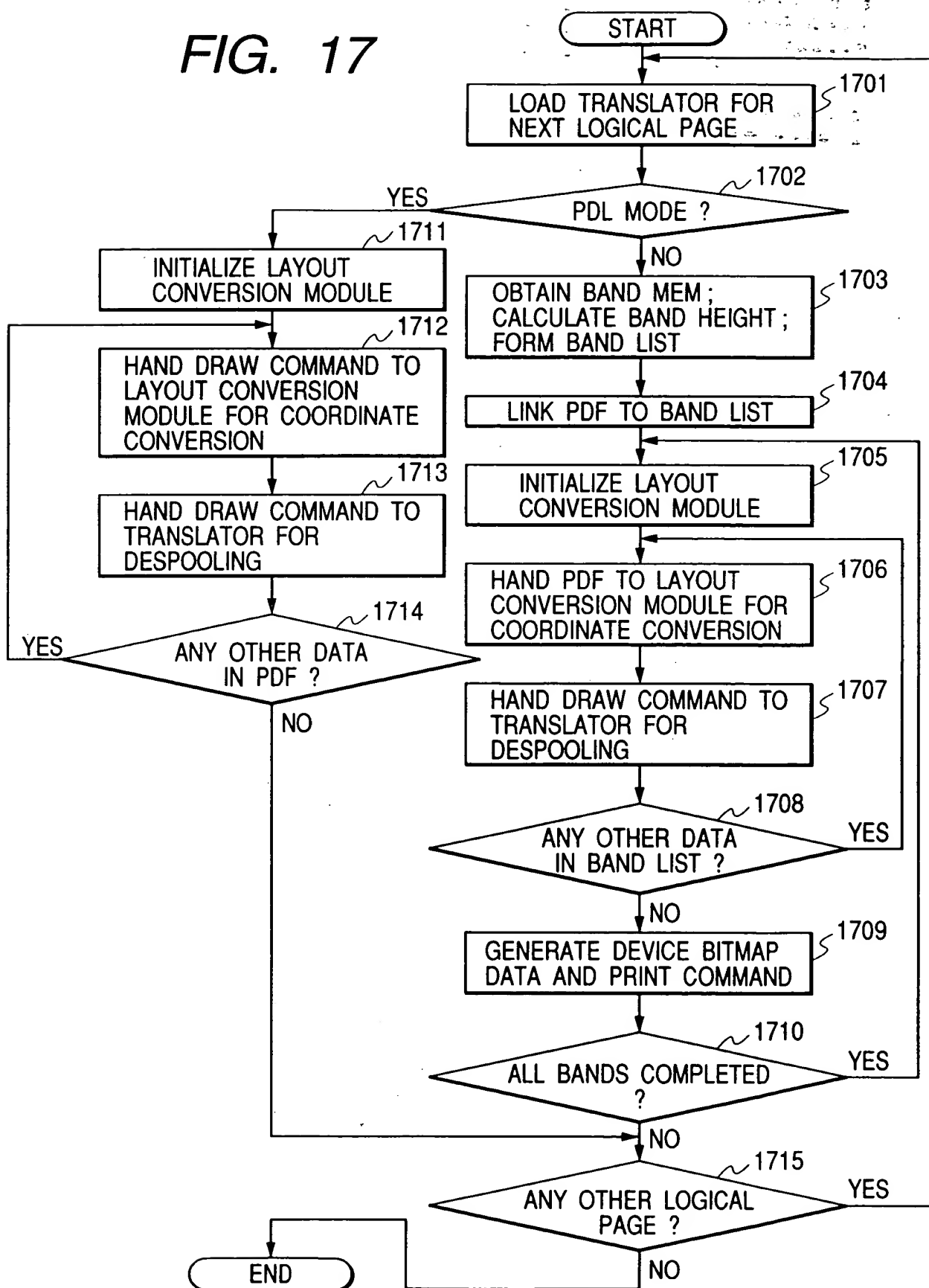
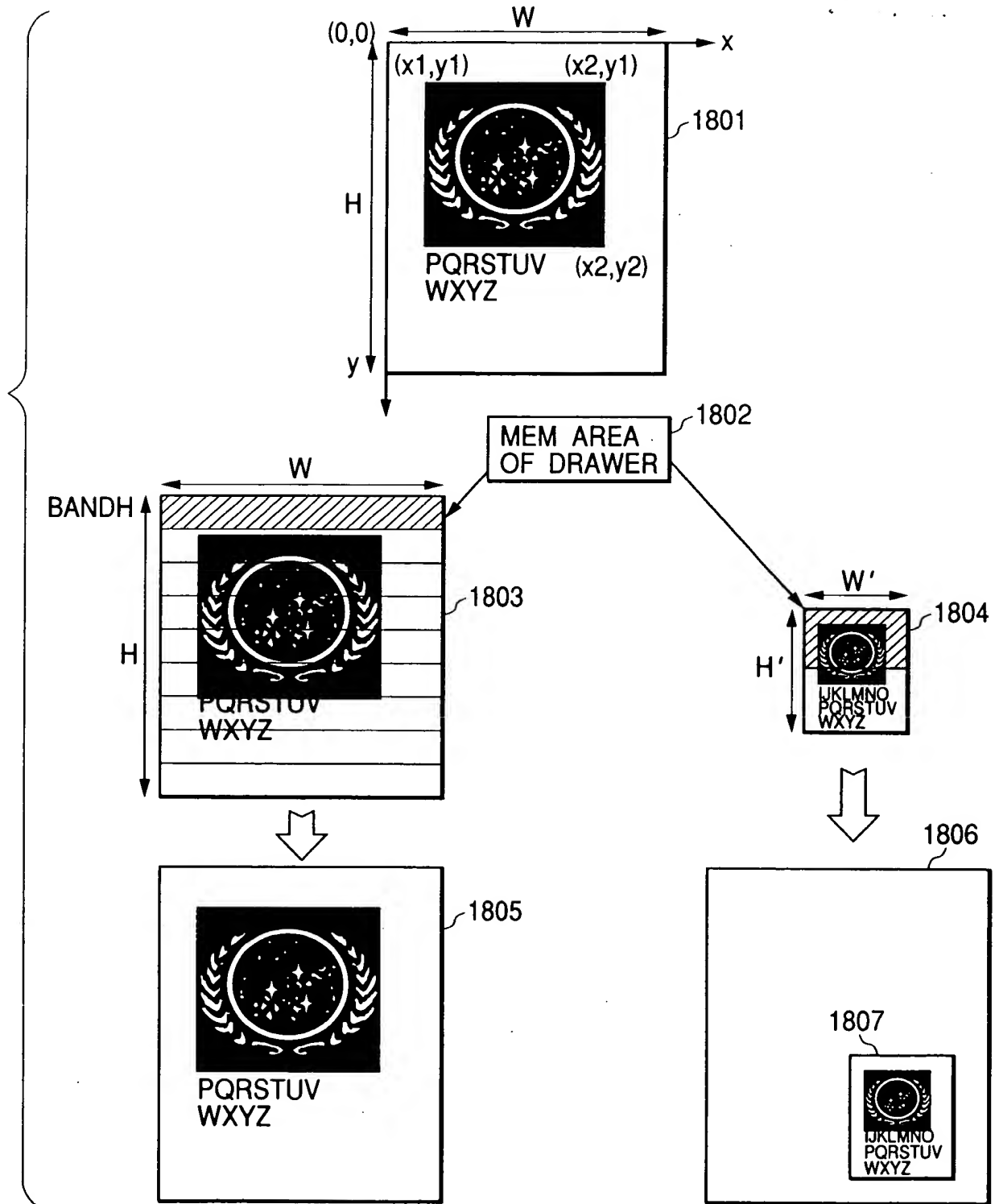


FIG. 18



00000000-00000000

FIG. 19

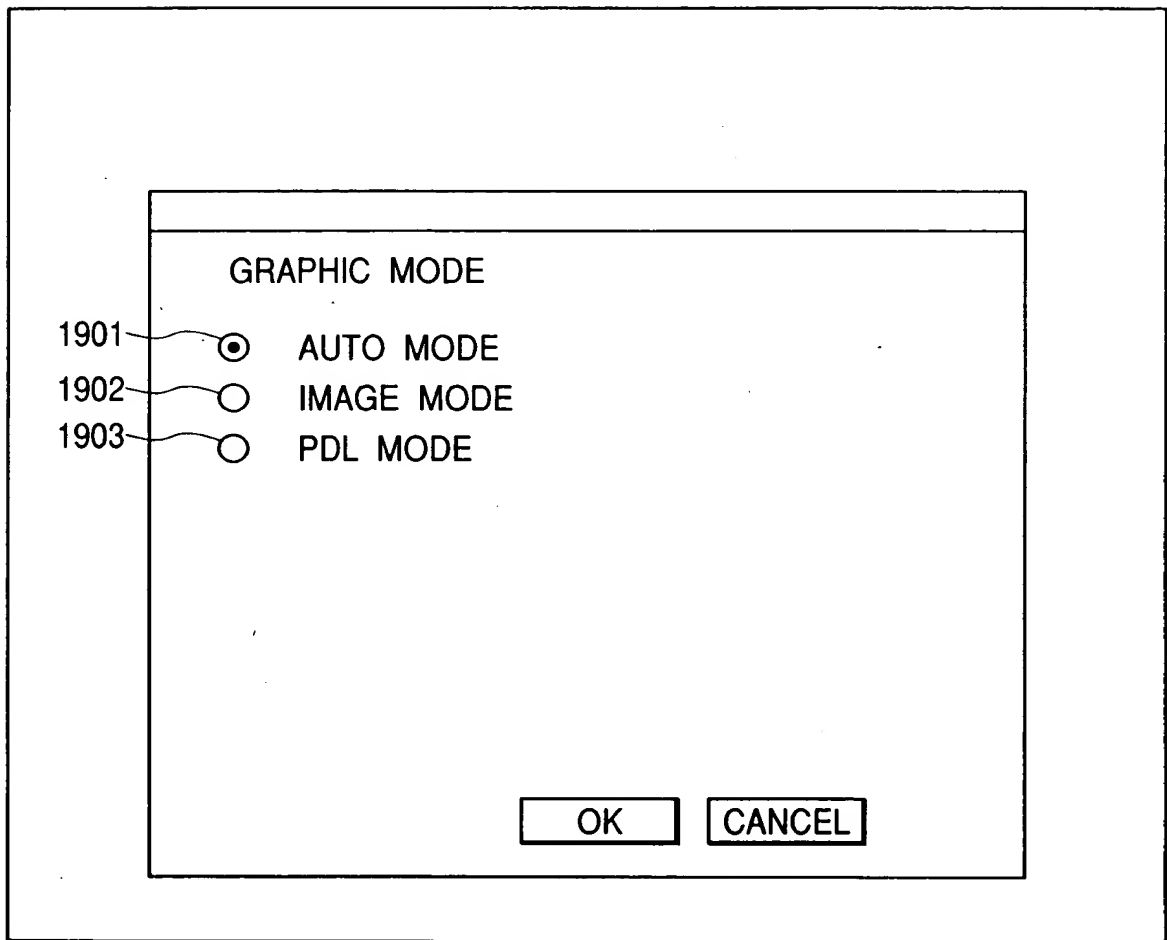
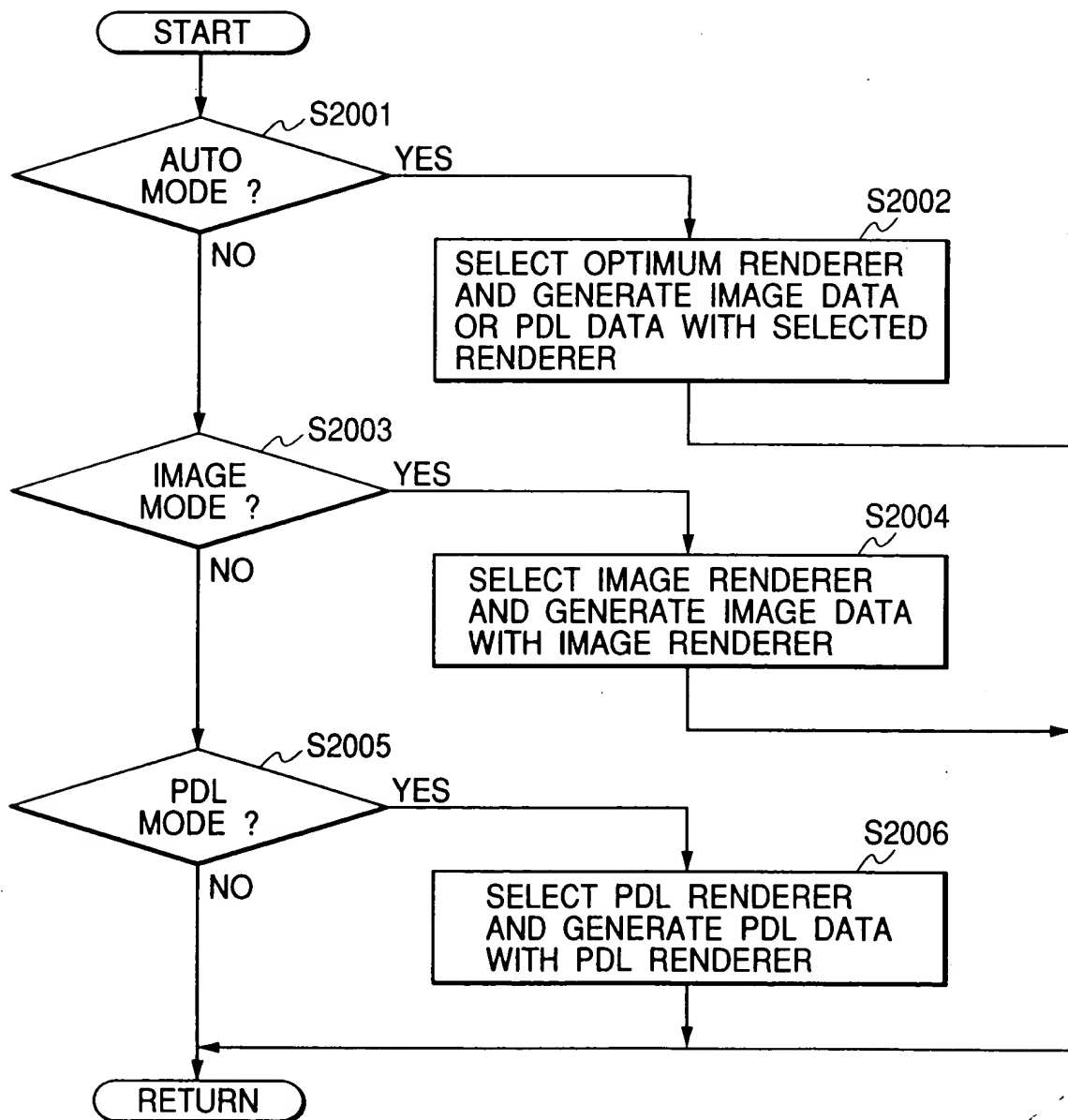


FIG. 20



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